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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

1 of 1

Complete if Known

Application Number	09/782,687
Filing Date	February 12, 2001
First Named Inventor	Savoj, Jafar
Art Unit	2633
Examiner Name	Dalzid E. Singh

Attorney Docket Number 019717-001110US

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U.S. PATENT DOCUMENTS

Examiner	Cite No. ¹	Document Number Number Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
MC	AA	US-4,535,459	08-13-1985	Hogge, Jr.	
MC	AB	US-5,301,196	04-15-1994	Ewen et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ³
		Country Code ³	Number ⁴ Kind Code ⁵ (if known)				
	AC						
	AD						

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
MC	AE	C.R. HOGGE, JR., "A Self Correcting Clock Recovery Circuit", IEEE Journal of Lightwave Technology, vol. LT-3, no. 6, December 1985	
MC	AF	E. MULLNER, "A 20 Gb/s Parallel Phase Detector and Demultiplexer Circuit in a Production Silicon Bipolar Technology with fT=25 GHz", Proc. IEEE BCTM, pp. 43-45, Oct. 1996	
MC	AG	M. RAU et al., "Clock/Data Recovery PLL Using Half-Frequency Clock", IEEE Journal of Solid-State Circuits, vol. 32, no. 7, July 1997	
MC	AH	K. NAKAMURA et al., "A 6 Gb/s CMOS Phase Detecting DEMUX Module Using Half-Frequency Clock", IEEE 1998 Symposium on VLSI Circuits Digest of Technical Papers	
MC	AI	M. WURZER et al., "40-Gb/s Integrated Clock and Data Recovery Circuit in a Silicon Bipolar Technology", IEEE BCTM 8.1, 1998	
MC	AJ	JAFAR SAVOJ et al., "A 10-Gb/s CMOS Clock and Data Recovery Circuit", IEEE 2000 Symposium on VLSI Circuits Digest of Technical Papers	

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Considered

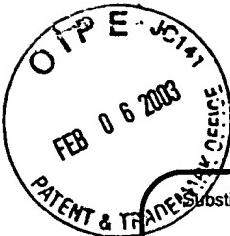
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¹ Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

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Examiner	Cite No. ¹	Number Kind Code ² (if known)	MM-DD-YYYY		
MC	AA	US-6,121,804	09-19-2000	Bryan et al.	

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		Country Code ³	Number ⁴ Kind Code ⁵ (if known)		
MC	AB	PCT	WO 01/06696	01-25-2001	Chang et al.
MC	AC	PCT	WO 01/63767	08-30-2001	Green

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Examiner Signature		Date Considered	11/3/05
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